



A Symposium on  
High Performance Interconnects



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- General Co-Chairs
  - Bryan Lyles
  - Anne Watters
- Program Co-Chairs
  - James Sterbenz
  - Dimitrios Stiliadis
- Tutorial Co-Chairs
  - James Sterbenz
  - Dimitrios Stiliadis
- Panel Chair
  - Fabrizio Petrini
- Local Chair & Webmaster
  - Liz Rogers
- Treasurer
  - Anne Watters
- Publications Chair
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  - Bill Boas
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- James Sterbenz
- Dimitrios Stiliadis
  - Program Chairs

# Program Committee

- Adnan Aziz,
  - University of Texas
- Alan Benner
  - IBM Research
- Andrea Bianco
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- Marcel Waldvogel
  - IBM Zurich Research
- Zhi-Li Zhang
  - University of Minnesota

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## Technical Sessions

- Authors:
  - Upload presentations to:
    - <http://www.gradebot.com/hoti/>
  - Meet with session chair
    - Meet at left of stage 15 minutes before session begins
    - Have presentation ready to run and provide bio to chair
- Presentation slides
  - Will be posted on-line at:
    - <http://www.hoti.org/>
- Full articles
  - Published in IEEE proceedings
    - Provided with registration
    - Additional copies available at registration desk



## Technical Program

- Received 31 papers
- Each paper got at least 3 reviews
- 12 were unconditionally accepted
  - 1 was withdrawn
- 3 were accepted with some modifications
- Introduced “Best-Paper Award” to acknowledge the most important contributions



## Best Paper Awards

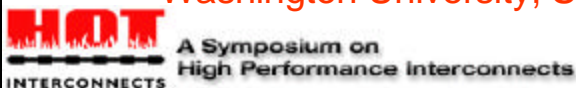
Two papers with perfect scores selected

- **Efficient Multi-Match Packet Classification with TCAM**  
F. Yu, R. Katz (U.C. Berkeley)
- **A Localized Congestion Control Mechanism for PCI Express Advanced Switching Fabrics**  
– V. Krishnan, D. Mayhew (Stargen)



## Keynotes

- **Bugger the Buffers: How we fell in love with them, and why we need a divorce**  
– Nick McKeown, Associate Professor  
– Stanford University
- **Virtualizing the Net - a strategy for enabling network innovation**  
– Jonathan Turner, Professor  
– Washington University, St. Louis



## Schedule for Wednesday, Aug 24, 2004

- 7:30 - 8:30 am : Breakfast
- 8:30 am : **Welcome**
  - Message from General Chair
  - Message from Program Chair
- 8:45 am : **Keynote (McKeown)**
  - Bigger the Buffers: How we fell in love with them, and why we need a divorce
- 9:45 am : **Break (Provided)**
- 10:15 : **Session 1**
  - System Level Interconnects
- Noon : **Lunch (Provided)**
- 1:30 pm : **Session 2**
  - Packet Classification and Lookup
- 3:00 pm : **Break (Provided)**
- 3:30 pm : **Session 3**
  - Routers and Switches
- 5 pm : **Dinner (Provided)**
- 6:30 pm : **Panel**
  - What are the future trends for high-performance interconnects in parallel computers



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## Schedule for Thursday, Aug 25, 2004

- 7:30 - 8:30 am : Breakfast
- 8:30 am : **Welcome**
  - Message from Tutorial Chair
- 8:45 am : **Keynote (Turner)**
  - Virtualizing the Net - a strategy for enabling network innovation
- 9:45 am : **Break (Provided)**
- 10:15 : **Session 4**
  - Security and Network Processors
- Noon : **Lunch (Provided)**
- 1:30 pm : **Panel 2**
  - Network Processors: Prospects for practical deployment
- 3:00 pm : **Break (Provided)**
- 3:30 pm : **Session 5**
  - Optical and Wireless Architectures



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## Friday Tutorials

- **T1:** Resilient Network Infrastructures for Global Grid Computing
  - Luca Valcarengi (morning session)
- **T2:** Internet Infrastructure Security
  - G. Manimaran (afternoon session)
- **T3:** High-Speed Networking: A Systematic Approach to High-Bandwidth, Low-Latency Communication
  - James Sterbenz (all-day session)



## Schedule for Friday, Aug 26, 2004

- 7:30 am : Breakfast (provided)
- 8:30 am - Noon
  - **Tutorial 1:**
    - High-Speed Networking: A Systematic Approach to High-Bandwidth Low-Latency Communication:
    - James Sterbenz
  - **Tutorial 2:**
    - Resilient Network Infrastructure for Global Grid Computing:
    - Luca Valcarengi
- 9:45 : Break (provided)
- Noon : Lunch (provided)
- 1:30 pm - 5:00 pm
  - **Tutorial 1 (continued):**
    - High-Speed Networking: A Systematic Approach to High-Bandwidth Low-Latency Communication:
    - James Sterbenz
  - **Tutorial 3:**
    - Internet Infrastructure Security
    - G. Manimaran
- 3:00pm : Break (provided)

